

Amendments to the Specification:

Please replace paragraph [0032] as follows:

The steps of a method or algorithm described in connection with the embodiments disclosed herein may be embodied directly in hardware, in a software module executed by a processor, or in a combination of the two. A software module may reside in Random Access Memory, RAM, flash memory, Read Only Memory, ROM, Electrically Programmable ROM, EPROM, Electrically Erasable Programmable ROM, EEPROM, registers, hard disk, a removable disk, a Compact-Disk ROM, CD-ROM, or any other form of non-transitory storage medium known in the art. An exemplary storage medium is coupled to the processor such the processor can read information from, and write information to, the storage medium. In the alternative, the storage medium may be integral to the processor. The processor and the storage medium may reside in an ASIC. The ASIC may reside in a user terminal. In the alternative, the processor and the storage medium may reside as discrete components in a user terminal.